



501.25958CV7

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: T. HOTTA, et al

Serial No.: 10/701,447

Filed: November 6, 2003

For: DATA PROCESSING SYSTEM GENERATING CLOCK SIGNAL  
FROM AN INPUT CLOCK PHASE LOCKED TO THE INPUT CLOCK  
AND USED FOR CLOCKING LOGICAL DEVICES

Group: 2115

Examiner: D. Butler

**AMENDMENT**

**MS Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

April 3, 2006

Sir:

In response to the Office Action dated December 2, 2005, please amend the above-identified application as follows.

**Amendments to the claims**

**Remarks** are included following the amendments.